TOSHIBA Photocoupler PHOTORELAY

TLP3220

Memory Tester Logic Tester

Measurement Instrument

The TOSHIBA TLP3220 is a super small-outline photorelay, suitable for surface-mount assembly. The TLP3220 consists of a GaAs infrared-emitting diode optically coupled to a photo-MOS FET and housed in a 4-pin package.

Features

• 4 pin SSOP (SSOP4) : 1.8 mm high, 1.27 mm pitch

• 1-Form-A

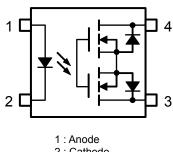
Peak off-state voltage : 100 V (Min.)
 Trigger LED current : 5 mA (Max.)
 On-state current : 80 mA (Max.)

On-state resistance : 14 Ω (Max.), 8 Ω (Typ.)
 Output capacitance : 8 pF (Max.), 6 pF (Typ.)
 Isolation voltage : 1500 Vrms (Min.)

Enlarged drawing is shown on page 4. JEDEC JEITA TOSHIBA 11-2B1

Weight: 0.03 g (Typ.)

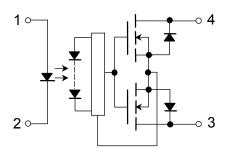
Pin configuration (top view)



2 : Cathode 3 : Drain

4 : Drain

Schematic



Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
	Forward current	IF	50	mA
Ω	Forward current derating (Ta ≥ 25°C)	ΔI _F /°C	-0.5	mA/°C
빌	Reverse voltage	V _R	5	V
	Junction temperature	Tj	125	°C
	Off-State output terminal voltage	V _{OFF}	100	V
Detector	On-State current	I _{ON}	80	mA
Dete	On-State current derating (Ta ≧ 25°C)	Δl _{ON} /°C	-0.8	mA/°C
	Junction temperature	Tj	125	°C
Storage temperature range		T _{stg}	−40~125	°C
Oper	rating temperature range	T _{opr}	-20~85	°C
Lead	soldering temperature (10 s)	T _{sol}	260	°C
Isola	tion voltage (AC, 1 min., R.H. ≦ 60%) (Note 1)	BVS	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1): Device considered a two-terminal device: Pins 1 and 2 shorted together, and pins 3 and 4 shorted together.

Caution

This device is sensitive to electrostatic discharge. When using this device, please ensure that all tools and equipment are earthed.

Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V_{DD}	_	_	80	V
Forward current	lF	10	_	30	mA
Operating temperature	T _{opr}	25	_	60	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
	Forward voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5 V	_	_	10	μΑ
	Capacitance	C _T	V = 0, f = 1 MHz	_	15	_	pF
Detector	Off-state current	loss	V _{OFF} = 80 V	_	_	200	pА
	On-state current	loff	V _{OFF} = 100 V			1	μΑ
ă	Capacitance	C _{OFF}	V = 0, f = 100 MHz, t < 1 s		6	8	pF

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Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED current	I _{FT}	I _{ON} = 80 mA	_	1	5	mA
Return LED current	I _{FC}	I _{OFF} = 1 μA	0.2	_	_	mA
On-state resistance	R _{ON}	$I_{ON} = 80 \text{ mA}, I_F = 10 \text{ mA}, t = 10 \text{ ms}$	_	8	14	Ω

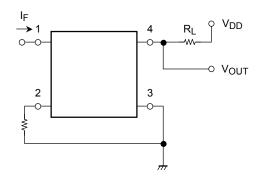
Isolation Characteristics (Ta = 25°C)

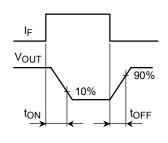
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Capacitance input to output	CS	V _S = 0 V, f = 1 MHz	_	0.6	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≦ 60%	5 × 10 ¹⁰	10 ¹⁴	_	Ω
		AC, 1 minute	1500	_	_	Vrms
Isolation voltage	BV_S	BV _S AC, 1 second (in oil) — 30	3000	_	VIIIIS	
		DC, 1 minute (in oil)	_	3000	_	Vdc

Switching Characteristics (Ta = 25°C)

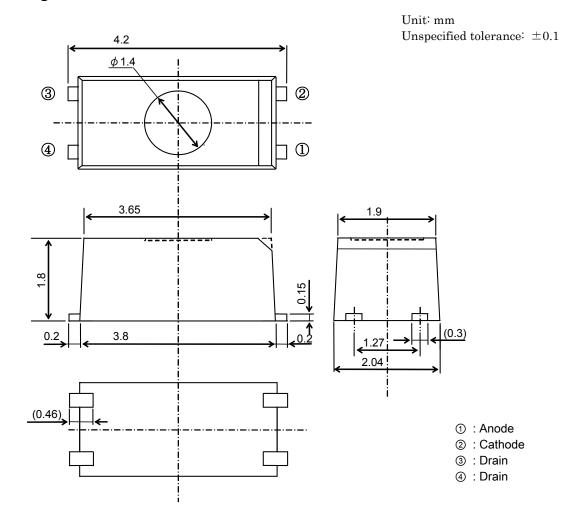
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Turn-on time	t _{ON}	$R_L = 200 \Omega$ (Note 2)	_	100	300	6
Turn-off time	tOFF	$V_{DD} = 20 \text{ V}, I_F = 5 \text{ mA}$	_	100	300	μS

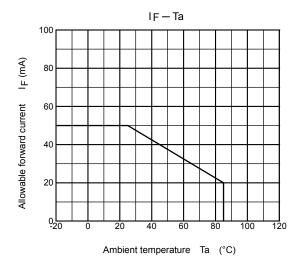
(Note 2): switching time test circuit

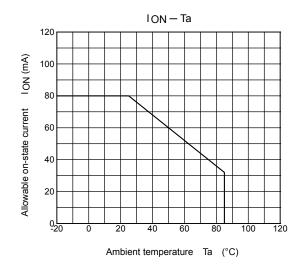


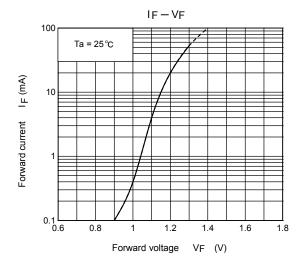


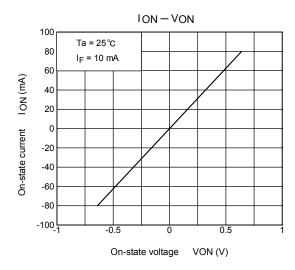
Outline Drawing

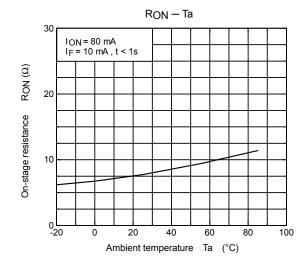


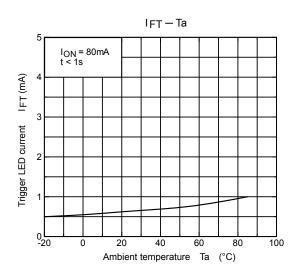


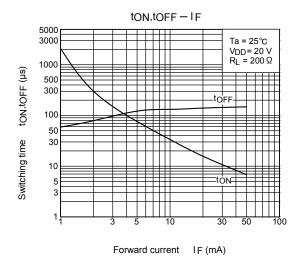


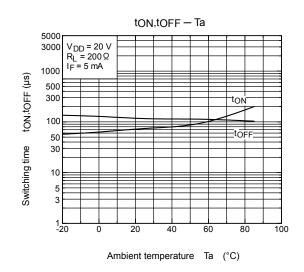


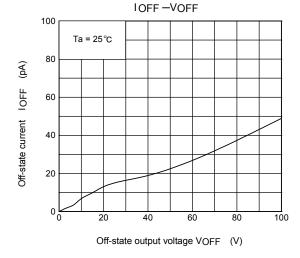


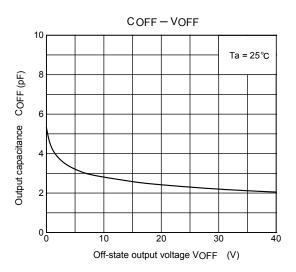












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